

Form PTO-1449 (modified) 6 2005 List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DKT. NO. 5181-96500 APPLICANT: Freyensee, et al. FILING DATE: November 9, 2001	SERIAL NO. 10/008,271 GROUP: 2128
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U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
T.P.		5,973,638	10/99	Robbins, et al.			
T.P.		5,466,200	11/95	Ulrich, et al.			
T.P.		5,838,948	11/98	Bunza, Geoffrey J.			
T.P.		6,289,398	9/01	Stallmo, et al.			
T.P.		5,889,954	3/99	Gessel, et al.			
T.P.		4,590,581	5/86	Widdoes, Jr., L. Curtis			
T.P.		5,247,650	9/93	Judd, et al.			
T.P.		5,893,155	4/99	Cheriton, David R.			
T.P.		2003/0093255	5/03	Freyensee, et al.			
T.P.		2003/0093256	5/03	Cavanagh, et al.			
T.P.		2003/0093252	5/03	Fankel, et al.			
T.P.		2003/0093253	5/03	Freyensee, et al.			
T.P.		2003/0093254	5/03	Frankel, et al.			
T.P.		2003/0093569	5/03	Sivier, et al.			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

T.P.	D1	Lee, K.C., "A Virtual Bus Architecture for Dynamic Parallel Processing Parallel and Distributed Systems," IEEE Transactions on, Vol. 4, Issue 2, Feb. 1993, pages 121-130.
T.P.	D2	Lee, K.C., "A Virtual Bust for Dynamic Parallel Processing Parallel Distributed Processing," 1990, Proceedings of the Second IEEE Symposium on December 9-13, 1990, pages 736-743.
T.P.	D3	Damani, Om et al., "Fault-Tolerant Distributed Simulation," May 1998, Proceedings. Twelfth Workshop, May 26-29, 1998, pages 38-45.
T.P.	D4	Tay, Seng Chuan, et al., "Speculative Parallel Simulation with an Adaptive Throttle Scheme," 1997, IEEE, pages 116-122.

EXAMINER: Thai Phan DATE CONSIDERED: 12/18/05

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